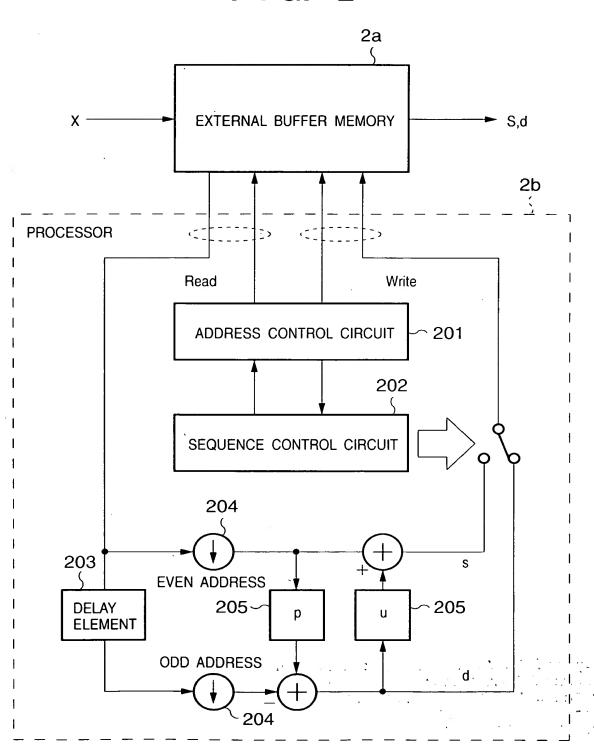
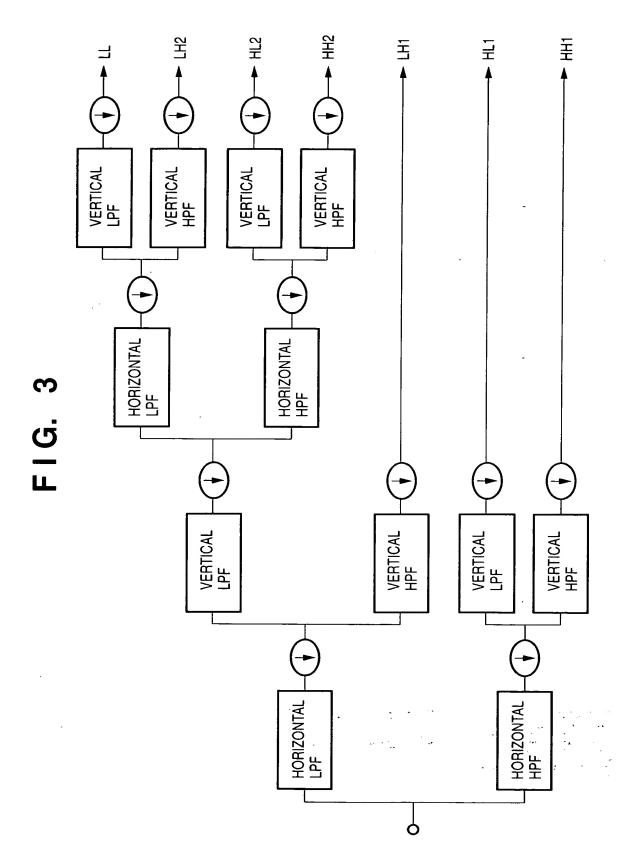


. 1

FIG. 2

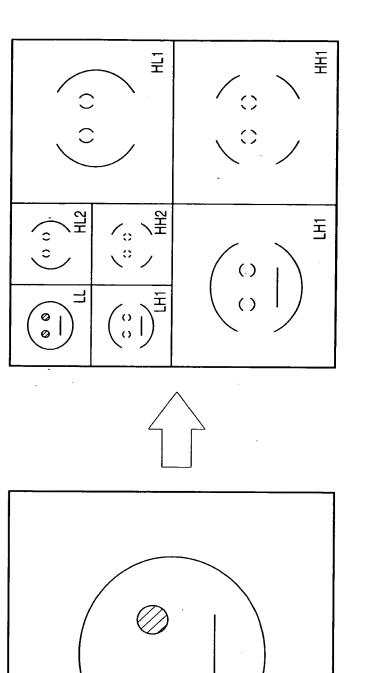


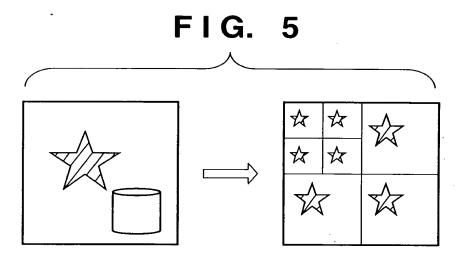
ķ.

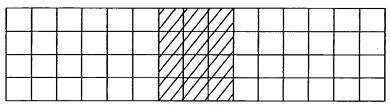


- Talo

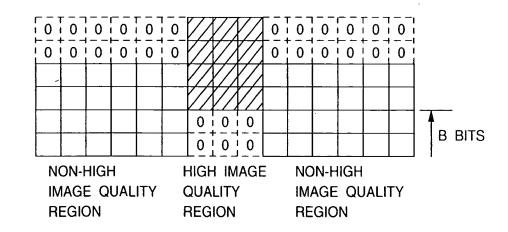


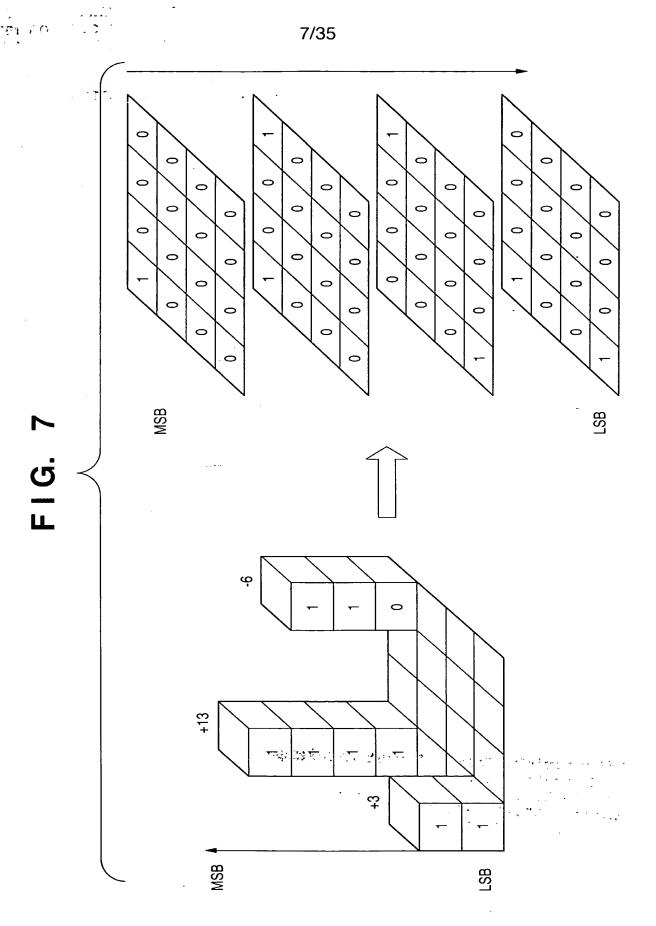


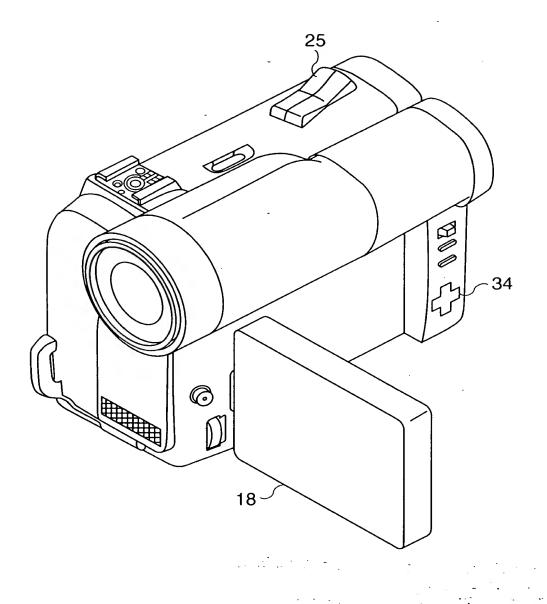


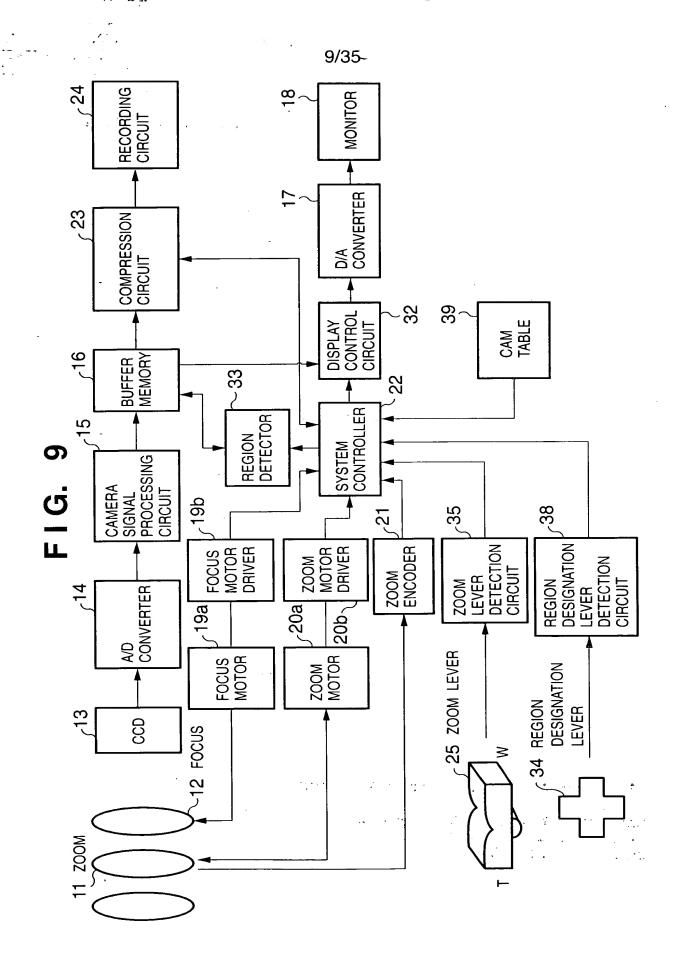


HIGH IMAGE QUALITY REGION









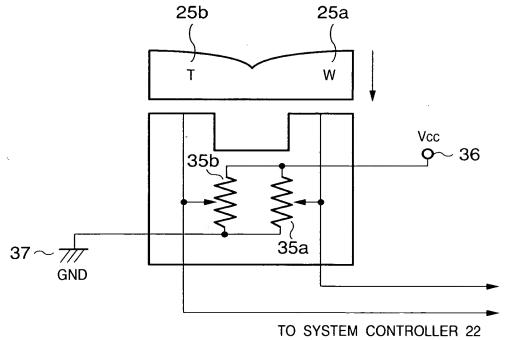
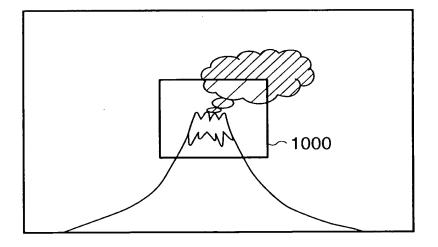


FIG. 11



12/35 ► OUTPUT 46 CODING CONTROL CIRCUIT BINARY ARITHMETIC CODING CIRCUIT BIT PLANE
DECOMPOSING 45 INDEX CHANGE CIRCUIT SYSTEM CONTROLLER COEFFICIENT SETTING CIRCUIT PROCESSING CIRCUIT 43 48 22 BIT SHIFT AMOUNT COMPUTATION CIRCUIT 42 OCCUPATION RATIO COMPUTATION CIRCUIT 40 TRANSFORMER WAVELET REGION INFORMATION IMAGE\_ INPUT\_

F1G. 12

FIG. 13

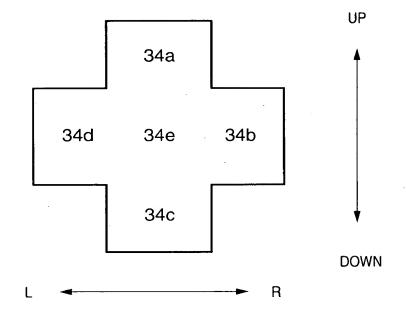


FIG. 14

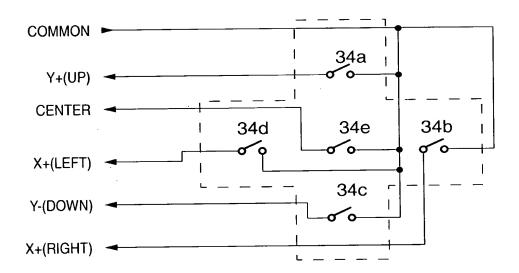


FIG. 15A

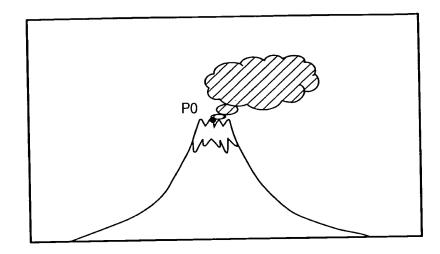


FIG. 15B

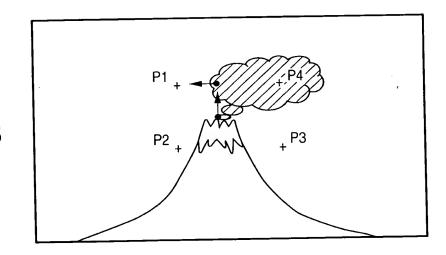
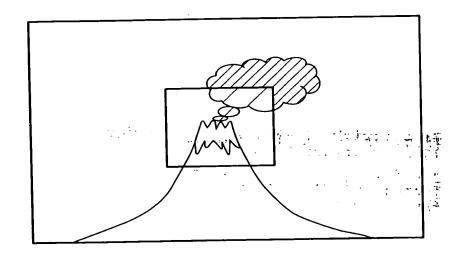
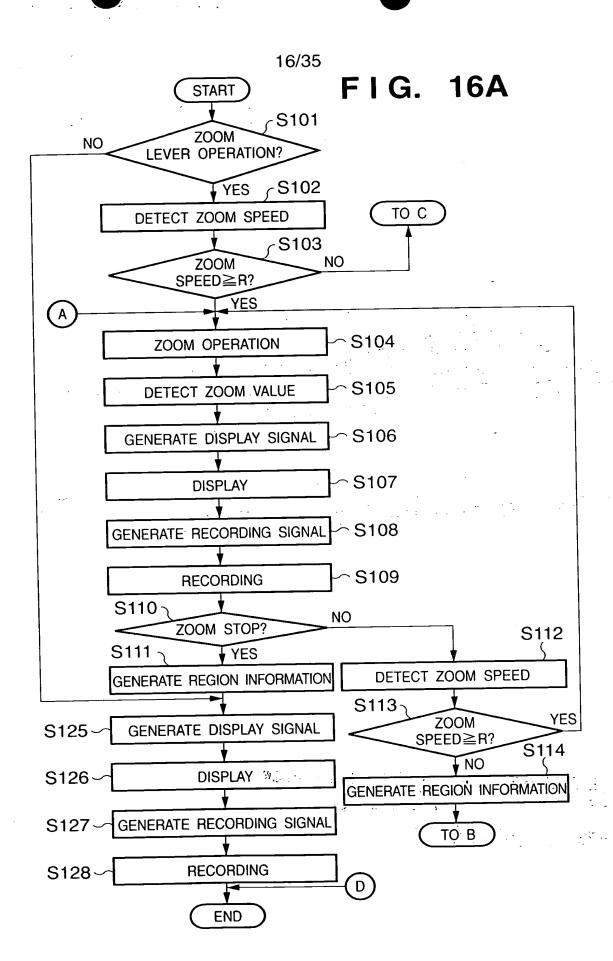


FIG. 15C





#### FIG. 16B

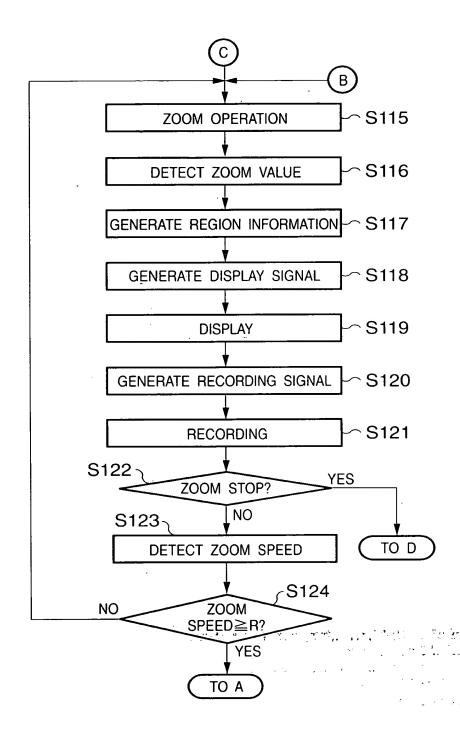


FIG. 17A

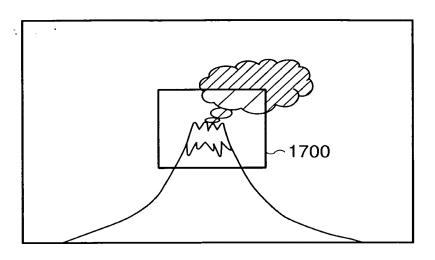


FIG. 17B

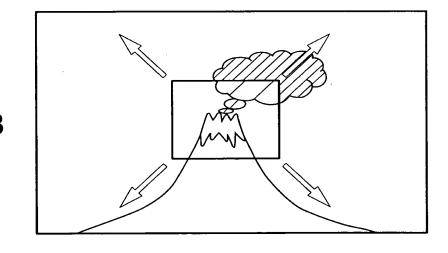


FIG. 17C

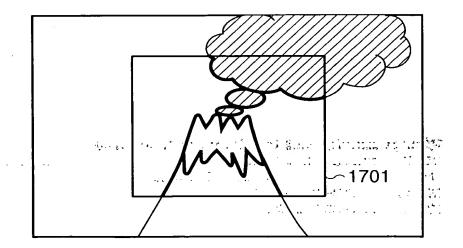
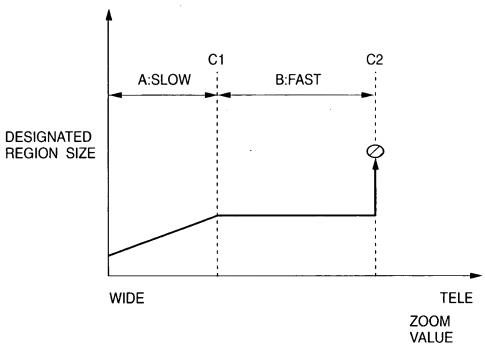
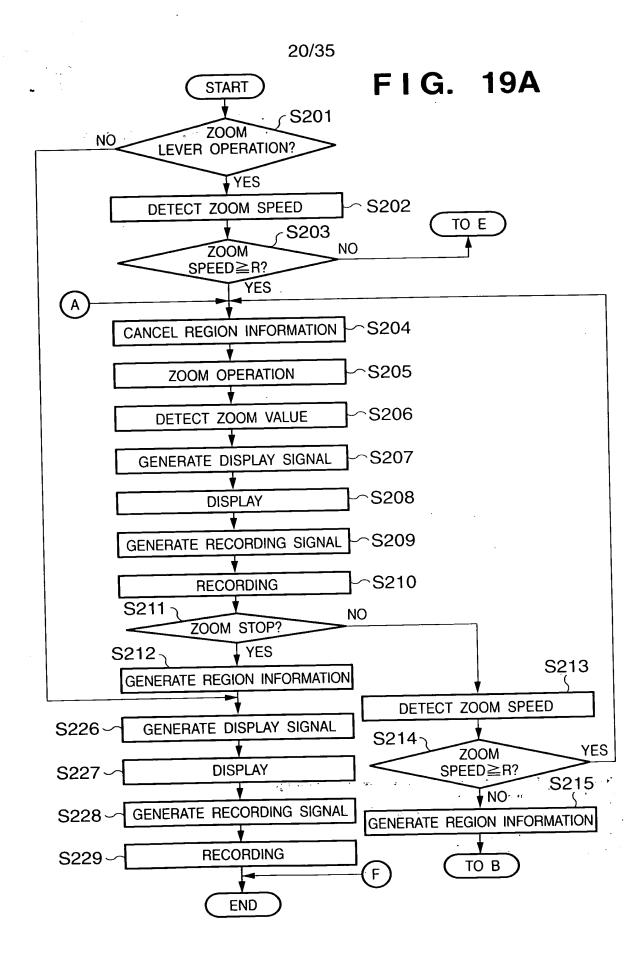


FIG. 18





## FIG. 19B

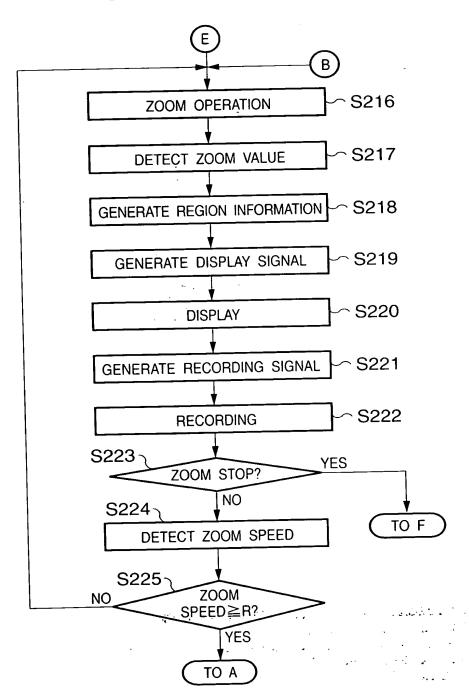


FIG. 20A

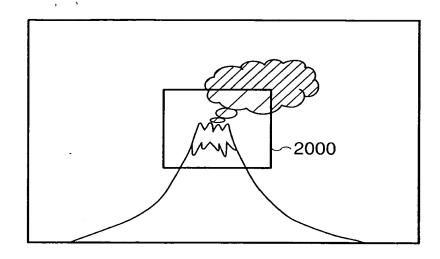


FIG. 20B

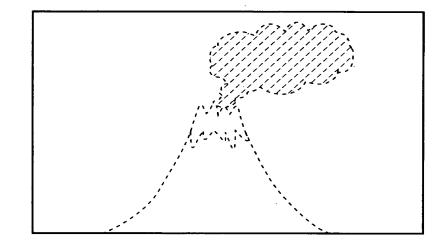
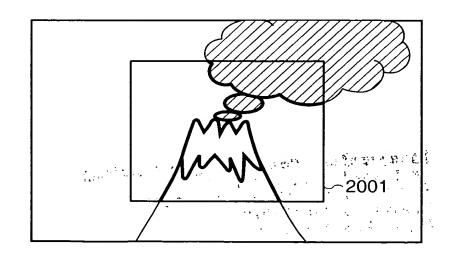
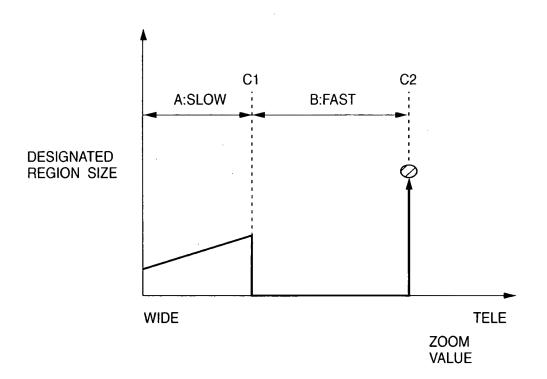


FIG. 20C





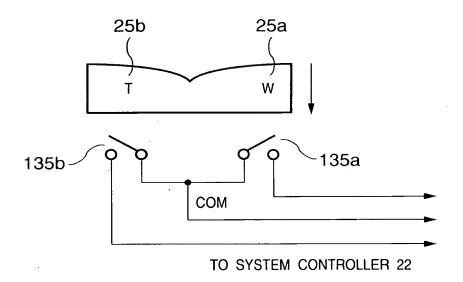


FIG. 23

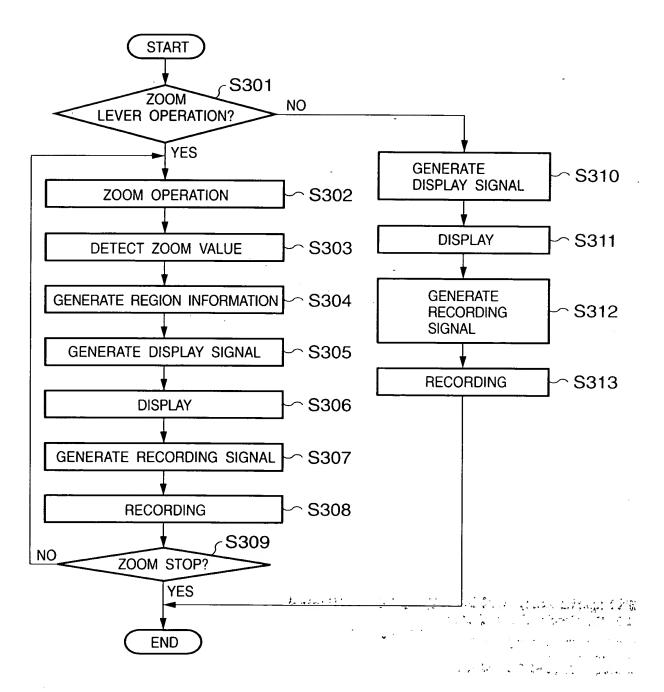


FIG. 24A

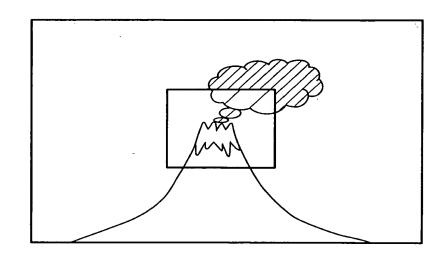


FIG. 24B

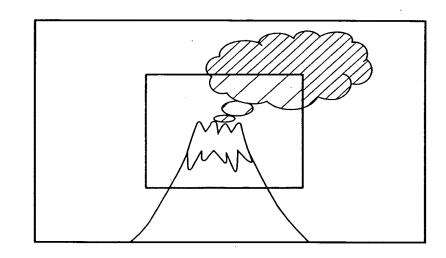


FIG. 24C

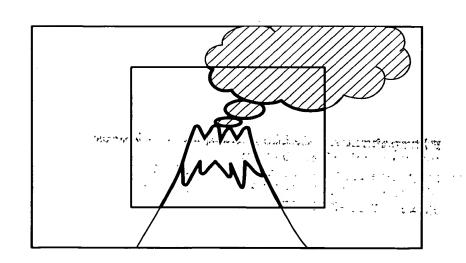
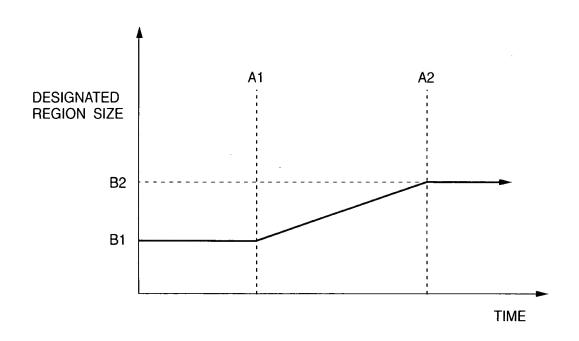


FIG. 25



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FIG. 26

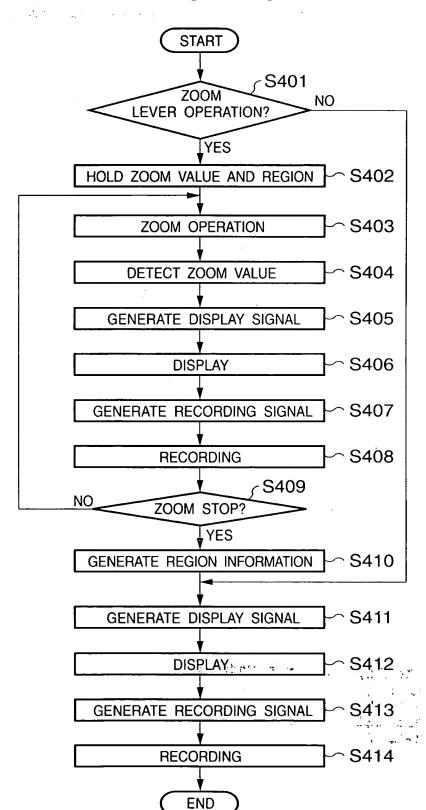


FIG. 27A

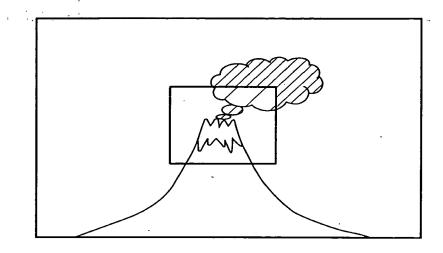


FIG. 27B

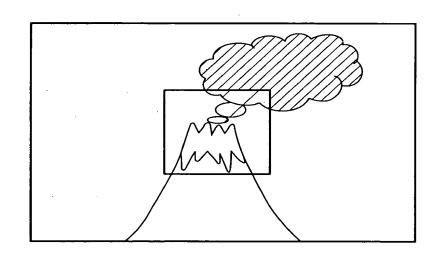
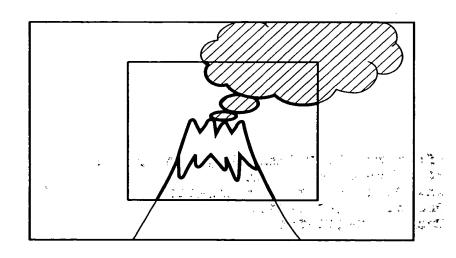
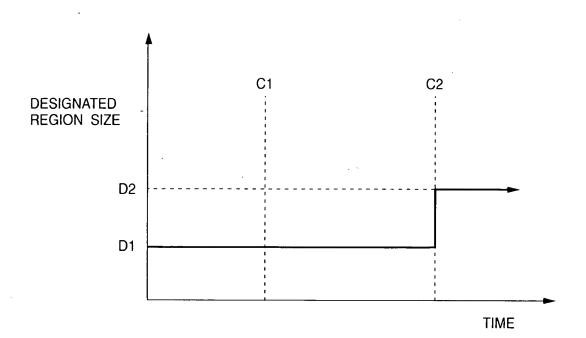


FIG. 27C







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FIG. 29

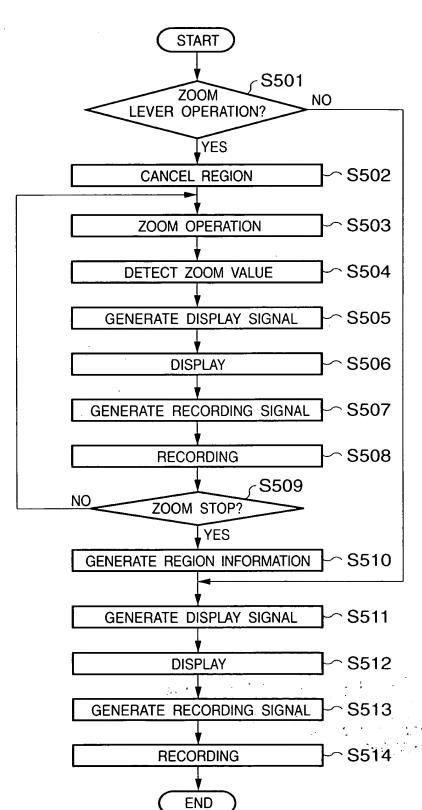


FIG. 30A

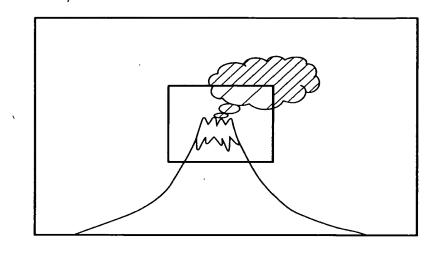


FIG. 30B

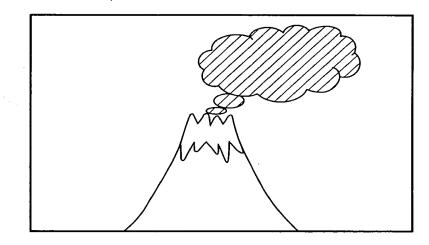


FIG. 30C

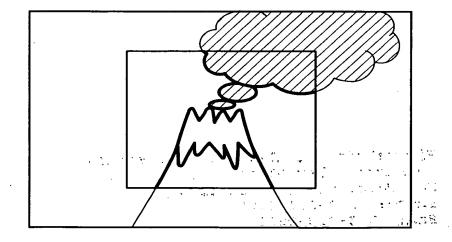


FIG. 31

